

## **REMARKS**

As a preliminary matter, Applicants thank the Examiner for the continued acknowledgement of allowable subject matter in claims 12 and 13.

Claims 1-3 again stand rejected as being anticipated by Kozakai et al. Applicants again respectfully traverse this rejection because the cited reference does not disclose (or suggest) a data processing device that interconnects by an internal bus the processor, the buffer manager, the program memory, and the shared memory, as in claim 1 of the present invention, as amended.

Applicants note the Examiner's remarks, on page 6 of the outstanding Office Action, indicate that the Examiner does not find the interconnection features of the present invention that were argued in Response A, filed June 14, 2004, to be clearly recited in the claims. Accordingly, claim 1 has been amended to clarify the interconnection features of the internal bus of the present invention. Applicants therefore incorporate the previous arguments from Response A herein, and respectfully request that the Examiner reconsider those arguments in light of these amendments to claim 1.

More particularly, these amended features of the present invention more clearly distinguish the present invention over the Kozakai reference. Claim 1 now more clearly recites that the internal bus interconnects the processor, the buffer manager, the program memory, and the shared memory. The internal bus functions as the data transfer path, and

therefore the processing time needed to read data from, or write data to, the shared memory can be shortened. Kozakai does not teach or suggest any such features.

Additionally, claim 1 has been further amended to more clearly recite that the selection made is from of a connecting line between the processor and the shared memory, and a connecting line between the buffer manager and the shared memory, based on a response to a select pattern register set on one of first and second conditions. Applicants submit that Kozakai fails to teach or suggest these additional clarified features of the present invention as well. Accordingly, for all of the foregoing reasons, as well as those of record, the Section 103 rejection of claim 1 (and its dependent claims 2-3) based on Kozakai is again respectfully traversed.

Claims 9-11 again stand rejected under 35 U.S.C. 103(a) as being unpatentable over Kozakai. In response, dependent claim 12 (containing allowable subject matter) has been cancelled without prejudice herein, and its allowable subject matter incorporated into independent claim 9. Accordingly, Applicants submit that independent claim 9 is in condition for allowance for at least these reasons. Claims 10-11 depend directly or indirectly from independent claim 9, and should therefore be in condition for allowance for at least the same reasons.

For all of the foregoing reasons, Applicants submit that this Application, including claims 1-3, 9-11, and 13 is in condition for allowance, which is respectfully requested. The Examiner is invited to contact the undersigned attorney if an interview would expedite prosecution.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By



Josh C. Snider

Registration No. 47,954

**Customer No. 24978**

April 1, 2005

300 South Wacker Drive  
Suite 2500  
Chicago, Illinois 60606  
Telephone: (312) 360-0080  
Facsimile: (312) 360-9315

P:\DOCS\0941\65970\894664.DOC